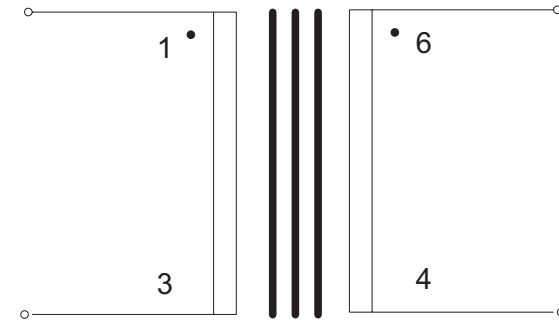
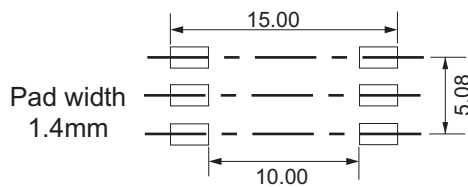
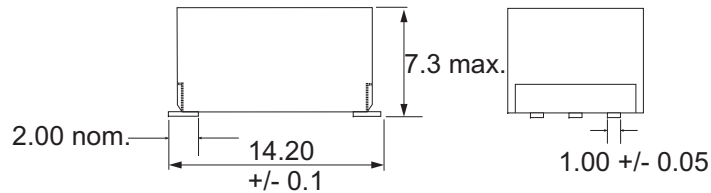


Part number

Z WW = date code  
 Z = year of manufacture (e.g. X=2009, Y=2010)  
 WW = week of manufacture



Electrical schematic



Suggested PCB layout

**Electrical specification:**

Ratio: 1 to 1  
 DC resistance (sum of windings): 191Ω - 258Ω  
 Impedance matching: 600 ohms to 600 ohms  
 Inductance (270mVrms, 100Hz parallel) Pins 1 - 3: 3.6H min.  
 Leakage inductance: (10mVrms, 200Hz series) pins 1 - 3: 4.1mH nom.  
 Return loss: (ref. 600 ohms) 200 to 4kHz: -18dB min.  
 Insertion loss: (ref. 600 ohms, 2kHz): 2dB max.  
 (ref. 430 ohms, 2kHz): 4dB max.  
 Frequency response: 200 - 4kHz: +/- 0.2dB  
 Longitudinal balance: 200Hz - 4kHz: 80dB min.  
 Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4: 1.00 +/- 1%  
 Distortion: 600Hz, 0dBm: -89dBm nom.  
 Saturation: <10Vrms, 65V peak, 50Hz  
 Hi-pot, primary to secondary: 5500VDC, 1mA for 2 seconds  
 Operating temperature range: -20 to +85°C  
 Storage temperature range: -40 to +125°C

Certified to EN60950-1: 2001

RoHS process compliant.

**Note: Do not pass DC current through windings.**



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DESCRIPTION	ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
Specification for OEP8000	1	29/04/04	CS		<b>OEP8000</b>
	7	01/04/09	CS		
	8	13/01/10	CS		
	9	21/10/10	CS		

Scale: 2 to 1

All dimensions in mm unless stated otherwise