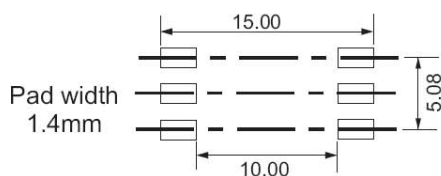
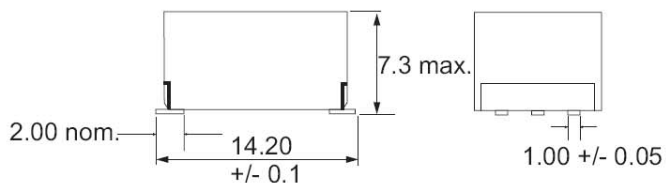
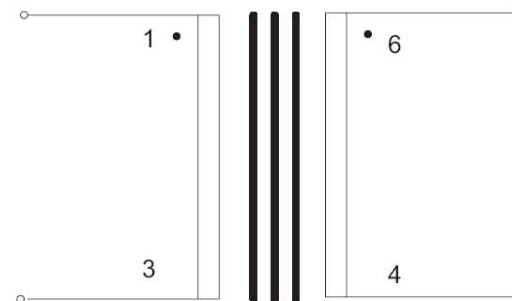


Part number

X ZZ = date code  
 X = year of manufacture (e.g. Y=2010)  
 ZZ = week of manufacture



Suggested PCB layout



Electrical schematic

**Electrical specification:**

Primary DC resistance:  $111\Omega \pm 15\%$   
 Secondary DC resistance:  $111\Omega \pm 15\%$   
 Impedance matching:  $600\Omega$  to  $600\Omega$   
 Inductance (270mVrms, 100Hz parallel) Pins 1 - 3: 3.6H min.  
 Leakage inductance: (10mVrms, 200Hz series) pins 1 - 3: 4.1mH nom.  
 Return loss: (ref. 600 ohms) 200 to 4kHz: -18dB min.  
 Insertion loss: (ref. 600 ohms, 2kHz): 2dB max.  
 (ref. 430 ohms, 2kHz): 4dB max.  
 Frequency response: 200 - 4kHz:  $\pm 0.2$ dB  
 Longitudinal balance: 200Hz - 4kHz: 80dB min.  
 Turns ratio (@ 6kHz, 0.1Vrms), pins 1 - 3 & 6 - 4: 1.00  $\pm 1\%$   
 Distortion: 600Hz, 0dBm: -100dBm nom.  
 Saturation: <10Vrms, 65V peak, 50Hz  
 Hi-pot, primary to secondary: 3.88kV, 1mA

Operating temperature range: -10 to +85°C  
 Storage temperature range: -40 to +125°C

**Note: Do not pass DC current through windings.  
 Certified to EN60950-1: 2001**

<p><b>Walters OEP Ltd.</b>          Unit 5, Oxonian Park, Langford Locks,          Kidlington, Oxfordshire. OX5 1FP          Tel: (01865) 855085 Fax: (01865) 855075          Website: www.oep.co.uk</p>	DESCRIPTION					ISSUE	DATE	DRAWN	CHECKED	DRAWING NUMBER
	Specification for OEP8034					1	21/06/05	CS		<b>OEP8034</b>
						4	30/06/08	CS		
						5	28/10/08	CS		
						6	13/01/10	CS		
						Scale: 2 to 1				